| L Number | Hits | Search Text | DB | Time stamp |
|----------|-------|---|-------------------|------------------|
| 1 | 5 | current-differenc\$3 NEAR circuit | USPAT; | 2003/01/17 18:36 |
| - | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| 2 | 7 | trans-impedance NEAR stage | USPĀT; | 2003/01/17 17:43 |
| ~ | , | | US-PGPUB; | |
| 1 | | | EPO; JPO; | |
| | | | IBM TDB | |
| 3 | 3578 | (differential common) NEAR2 feedback | USPAT; | 2003/01/17 18:37 |
| ' | 3310 | (directified continuity 142) file reedback | US-PGPUB; | |
| | , 1 | | EPO; JPO; | - |
| | | | IBM_TDB | |
| | 702 | (differential common) NEAR2 (feedback ADJ circuit) | USPAT; | 2003/01/17 18:38 |
| 4 | /02 | (unitational common) (4D/M2 (technical AD) enemy | US-PGPUB; | 1 |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| 6 | 0 | ((differential NEAR2 (feedback ADJ circuit)) WITH (common NEAR2 (feedback ADJ circuit))) and current-steering | USPAT; | 2003/01/17 18:49 |
| | | | US-PGPUB; | 2003/01/17 10.49 |
| | | | | |
| | | | EPO; JPO; | |
| | | (differential NEAR2 (feedback ADJ circuit)) WITH (common NEAR2 | IBM_TDB | 2002/01/17 19:42 |
| 5 | 37 | | USPAT; | 2003/01/17 18:42 |
| | | (feedback ADJ circuit)) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | 2002/01/17 19 52 |
| 7 | 25 | current-steering ADJ circuit | USPAT; | 2003/01/17 18:53 |
| | | - | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | 2002/01/15 10 55 |
| 8 | 4 | (current-steering ADJ circuit) and (reference ADJ circuit) | USPAT; | 2003/01/17 18:53 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 1 | ("4021747").PN. | USPAT; | 2003/01/17 12:52 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | 1 |
| | | | IBM_TDB | |
| _ | 17387 | circuit same (low NEAR voltage) same (high NEAR voltage) | USPAT; | 2003/01/17 12:57 |
| | | | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| 1_ | 252 | (circuit same (low NEAR voltage) same (high NEAR voltage)) and (field | USPAT; | 2003/01/17 13:01 |
| 1 | 232 | NEAR transistor) | US-PGPUB; | |
|] | | 1.2.2.3 | EPO; JPO; | |
| | | | IBM_TDB | |
| | 238 | circuit same (low NEAR voltage NEAR (input terminal)) same (high | USPAT; | 2003/01/17 14:06 |
| - | 230 | NEAR voltage NEAR terminal) | US-PGPUB; | |
| | | The total transfer of the terminary | EPO; JPO; | |
| | | | IBM_TDB | |
| _ ' | 2 | (circuit same (low NEAR voltage NEAR (input terminal)) same (high | USPAT; | 2003/01/17 14:23 |
| - | - | NEAR voltage NEAR terminal)) and (field NEAR transistor) | US-PGPUB; | |
| | | TALITA VOITAGE TALITA (CHIMINAL)) and (Hold Talita Chambiolos) | EPO; JPO; | |
| | | | IBM TDB | |
| | 0.4 | ((circuit same (low NEAR voltage) same (high NEAR voltage)) and (field | USPAT; | 2003/01/17 14:03 |
| - | 84 | NEAR transistor)) and (source NEAR2 drain NEAR2 (gate control)) | US-PGPUB; | |
| | | NEAR transision)) and (source NEAR2 drain NEAR2 (gate conditi)) | EPO; JPO; | |
| | | | IBM_TDB | |
| | 2: | (circuit same (low NEAR voltage NEAR (input terminal)) same (high | USPAT; | 2003/01/17 14:22 |
| 1 - | 21 | VEAD and to so NEAD terminal)) and (course NEAD2 drain MEAD2 | US-PGPUB; | |
| | | NEAR voltage NEAR terminal)) and (source NEAR2 drain NEAR2 | EPO; JPO; | |
| | | (gate control)) | 1 | |
| | | A SAMONE ALL AND AD A SIDAD INC. A SHIPLE ALD AD | IBM_TDB USPAT; | 2003/01/17 14:09 |
| - | 2 | · · · · · · · · · · · · · · · · · · · | 1 | 2003/01/17 14.03 |
| | | voltage NEAR terminal) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | <u> </u> |

| | | | r | 1 |
|---|------|---|-----------|------------------|
| - | 0 | transistor WITH (low NEAR voltage NEAR input) WITH (high NEAR | USPAT; | 2003/01/17 14:10 |
| | | voltage NEAR terminal) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 2 | transistor SAME (low NEAR voltage NEAR input) SAME (high NEAR | USPAT; | 2003/01/17 14:14 |
| | | voltage NEAR terminal) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 6 | circuit SAME (low NEAR voltage NEAR input) SAME (high NEAR | USPAT; | 2003/01/17 14:16 |
| | | voltage NEAR terminal) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 2 | circuit SAME transistor SAME (low NEAR voltage NEAR input) SAME | USPAT; | 2003/01/17 14:19 |
| | | (high NEAR voltage NEAR terminal) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 5905 | circuit SAME transistor SAME (low NEAR voltage) SAME (high NEAR | USPAT; | 2003/01/17 14:20 |
| | | voltage) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 1564 | circuit WITH transistor WITH (low NEAR voltage) WITH (high NEAR | USPAT; | 2003/01/17 14:21 |
| | | voltage) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 297 | (circuit WITH transistor WITH (low NEAR voltage) WITH (high NEAR | USPAT; | 2003/01/17 14:22 |
| | | voltage)) and (source NEAR2 drain NEAR2 (gate control)) | US-PGPUB; | |
| | | | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 123 | ((circuit WITH transistor WITH (low NEAR voltage) WITH (high NEAR | USPAT; | 2003/01/17 14:23 |
| | | voltage)) and (source NEAR2 drain NEAR2 (gate control))) and (field | US-PGPUB; | |
| | | NEAR effect NEAR transistor) | EPO; JPO; | |
| | | | IBM_TDB | |
| - | 115 | (((circuit WITH transistor WITH (low NEAR voltage) WITH (high | USPAT; | 2003/01/17 17:25 |
| | | NEAR voltage)) and (source NEAR2 drain NEAR2 (gate control))) and | US-PGPUB; | |
| | | (field NEAR effect NEAR transistor)) and (input terminal) | EPO; JPO; | |
| | | | IBM_TDB | |